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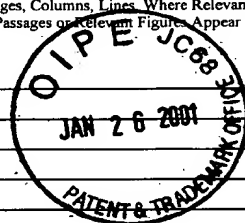
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	08/799,506
		Filing Date	February 12, 1997
		First Named Inventor	YAMAZAKI, S.
		Group Art Unit	2822
		Examiner Name	Wilczewski
		Attorney Docket Number	740756-1630
Sheet		of	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
MW		5,043,299		Chang et al.	08/27/1991	
MW		5,308,998		Yamazaki et al.	05/03/1994	
MW		5,965,904		Ohtani et al.	10/12/1999	
MW		5,966,594		Adachi et al.	10/12/1999	



FOREIGN PATENT DOCUMENTS								
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
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MW		SILICON PROCESSING FOR THE VLSI ERA, Vol. 1, pp. 61-65, 1994, WOLF, et al.	
MW		SILICON PROCESSING FOR THE VLSI ERA, Vol. 3, p. 648, 1995, WOLF, et al.	

Examiner Signature	M. Wilczewski	Date Considered	11/2001
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